

B6
Cmcd
capacitances in said second group of picture elements, during a selection period of said first and second group of picture elements.

IN THE ABSTRACT

Please delete the abstract added by the preliminary amendment of August 18, 1994, and insert the following new abstract:

--Abstract of the Disclosure

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A matrix panel display apparatus includes plural signal lines and plural scanning lines intersecting each other and, near each intersection point, a picture element including a picture element electrode, a counter electrode, a display medium disposed between the picture element electrode and the counter electrode, and a transistor for applying image signals from the signal line to the picture element electrode. The transistor is controlled in response to scanning signals received on a scanning line. An auxiliary signal generating circuit generates auxiliary signals for increasing effective voltages of the image signals and applies the auxiliary signals to the picture elements while each of the transistors is in a non-conducting state and each of the picture elements is not selected. Preferably, the auxiliary signal generating circuit applies the auxiliary signals to the picture elements during a predetermined period in which all of the transistors